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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/085,632 02/27/2002 PAT 6,639,287 ✓

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN JP 2001-052099 02/27/2001 ✓

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 11/13/2003

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	Examiner's Signature Initials	JAPAN	27	2	1

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## TITLE

METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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